

Access Control List Processing in Hardware
(1997) (Make Corrections)
Dominique Alessandri

View or download:
ec.eurecom.fr/~ale...nginhardware.pdf
Cached: [PS.gz](#) [PS](#) [PDF](#) [DjVu](#) [Image](#) [Update](#) [Help](#)

From: ec.eurecom.fr/~alessandri/ (more)
(Enter author homepages)

CiteSeer [Home](#) [Search](#) [Context](#) [Related](#)

[\(Enter summary\)](#)

Rate this article: [1](#) [2](#) [3](#) [4](#) [5](#) (best)
[Comment on this article](#)

Abstract: This diploma-thesis analyses how to process Access Control Lists (ACLs) in hardware at wirespeed using Content Addressable Memory (CAM). Also another approach based on a tree-structure is evaluated. ACLs are a major performance issue in high speed routers. Traditionally ACLs have been processed sequentially in software. The result of this method is that checking complex ACLs limits router performance to about 10,000 packets per second. The approach analyzed and described here allows parallel...
[\(Update\)](#)

Similar documents based on text: [More](#) [All](#)

0.4: "Agent A, Can You Pass the Salt?" - The Role of.. - Holmback, Greaves.. (1998) [\(Correct\)](#)

0.4: The current landscape of Agent Communication Languages - Labrou, Finin, Peng (1999) [\(Correct\)](#)

0.3: Agent Communication Languages: The Current Landscape - Labrou, Finin, Peng (1999) [\(Correct\)](#)

BibTeX entry: [\(Update\)](#)

```
@misc{ alessandri-access,
author = "Dominique Alessandri",
title = "Access Control List Processing in Hardware",
url = "citeseer.ist.psu.edu/alessandri97access.html" }
```

Citations (may not include all citations):

3 Bjarne Stroustrup (context) - Programming, nd

[Online articles have much greater impact](#) [More about CiteSeer.IST](#) [Add search form to your site](#) [Submit documents](#) [Feedback](#)

CiteSeer.IST - Copyright [NEC](#) and [IST](#)

L Number	Hits	Search Text	DB	Time stamp
1	191037	"content addressable memory" or CAM	USPAT	2004/03/24 07:08
2	108	("content addressable memory" or CAM) and (wirespeed or "wire-speed" or "wire speed")	USPAT	2004/03/24 07:09
3	27	((("content addressable memory" or CAM) and (wirespeed or "wire-speed" or "wire speed")) and "access control"	USPAT	2004/03/24 07:09

L Number	Hits	Search Text	DB	Time stamp
1	191037	"content addressable memory" or CAM	USPAT	2004/03/24 07:08
2	108	("content addressable memory" or CAM) and (wirespeed or "wire-speed" or "wire speed")	USPAT	2004/03/24 07:09
3	27	((("content addressable memory" or CAM) and (wirespeed or "wire-speed" or "wire speed")) and "access control"	USPAT	2004/03/24 07:11
4	33	bechtolsheim-andrea\$.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/03/24 07:11
5	12	cheriton-david\$.in.	USPAT; US-PPGPUB; EPO; JPO; DERWENT	2004/03/24 07:11

L Number	Hits	Search Text	DB	Time stamp
1	983	packet adj (classif\$8 or filter\$3)	USPAT	2004/03/24 07:17
2	191037	cam or "content addressable memory"	USPAT	2004/03/24 07:17
3	91	(packet adj (classif\$8 or filter\$3)) and (cam or "content addressable memory")	USPAT	2004/03/24 07:17
4	44	((packet adj (classif\$8 or filter\$3)) and (cam or "content addressable memory")) and "access control"	USPAT	2004/03/24 07:17